Please amend the specification as follows:

Page 11, paragraph beginning on line 16, amend as follows:

The MUX 10-4 selects (i) a signal input ~~form~~ from terminal "co" when a signal B is 0, or (ii) a signal output from FF 10-2 when the signal B is 1. The MUX 10-4 outputs the selected signal as signal UO via terminal "uo".

Page 12, paragraph beginning at line 7, amend as follows:

Core 2 has first to fourth input terminals and ~~first to~~ third output terminals. Similar to the case of the core 1, the first to third ~~input~~ terminals and the first to third output terminals are connected to corresponding terminals ~~"ui" and "uo"~~ "ci" and "cu" of the test circuits 13 to 15. The fourth ~~input~~ terminal is connected to terminal "2ci" of circuit 20 which is explained later.

Page 15, paragraph beginning on line 12, amend as follows:

First, the operation of testing core 1 will be explained. If TAP 60 outputs test mode signals B = 1, T1 = 1, and T2 = 0 to the relevant sections, then (i) the circuit 42, receiving test mode signal T2 = 0, outputs 0 and (ii) the circuit ~~42~~ 41, receiving test mode signal T1 = 1, outputs a signal according to the signal output from the test circuit 12, via the OR circuit 48 to terminal POT1. Accordingly, the signal output from the circuit 41 can be observed via the parallel test terminal POT1.

Page 15, paragraph beginning on line 24, amend as follows:

Next, if TAP 60 outputs test mode signal S = 0, then in the test circuits 11 and 12, signal CO output from core 1 is input via ~~terminal "ui"~~ MUX 10-1 to FF10-2. After that, when TAP 60 outputs test mode signals S = 1 again, the test pattern can be serially output via parallel test terminal POT1.

Page 22, paragraph beginning on line 11, amend as follows:

Below, in the test circuit forming method, the method determining the serial length (called "user scan serial length") and the parallel number (called "user scan parallel number") with respect to scan FFs 31 to 36 will be explained. First the user scan parallel number is determined by dividing the number of parallel test terminals (here, 2) by 2; thus, 1 is the user scan parallel number. The user scan serial length is determined in a manner such that: the number of input terminals and the number of output terminals of core 1 are 2; thus, the number of terminals is 2, while the number of terminals of core 2 is 4 which is the number of the input terminals. The number of scan FFs in the user circuits is 6; thus, these numerals 2, 4, and 6 are added and the result is 12, which is then divided by 1, the user scan parallel number, so that the user scan serial length is determined as 12.

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Page 23, paragraph beginning on line 22, amend as follows:

Next, the terminal ~~"si"~~ "so" of a circuit (test circuit 10 or circuit 20), whose terminal "si" has been connected but whose terminal "so" has not yet been connected, is connected to parallel test output terminal POT1 (see step S209). Here, this parallel test output terminal is not used by another core (see step S210); thus, the operation proceeds to step S212. In the step S212, the parallel number of core 1 is 1; thus, the operation proceeds to step S213.

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